



## Customer Information Notification

202102037I : PCA8561 Datasheet 9 Clock Pulses after Power-On Requirement

**Note:** This notice is NXP Company Proprietary.

**Issue Date:** Mar 17, 2021 **Effective date:** Mar 26, 2021

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### Management summary

Updated sections 7.3 "Starting and Resetting the PCA8561", 7.3.3 "Hardware Reset: RST Pin" and 15.1 "Power-on Reset" of the Datasheet, to ensure the users send nine clock pulses immediately after power-on (see also UM10204) for the proper device operation; if the POR does not work properly.

### Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Process	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Equipment	<input checked="" type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Location	<input type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input type="checkbox"/> Other			

## PCN Overview

### Description

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Updated sections 7.3 "Starting and Resetting the PCA8561", 7.3.3 "Hardware Reset: RST Pin" and 15.1 "Power-on Reset", as follows:

7.3 Starting and Resetting the PCA8561  
Added "See also application information" comment.

7.3.3 Hardware Reset: RST pin  
Removed "The bus interface is initialized" comment.

15.1 Power-on Reset  
The built-in POR block acts on the rising edge of the VDD supply voltage. Depending on the VDD rising edge in the application, the POR may not work properly. Therefore to ensure proper device operation it is required to send nine clock pulses immediately after power-on (see also UM10204).

### Reason

The intent of these updates is to ensure that the customers send nine clock pulses immediately after power-on (see also UM10204) for the proper device operation; if the POR does not work properly.

**Identification of Affected Products**

Product identification does not change

**Anticipated Impact on Form, Fit, Function, Reliability or Quality**

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No Impact on form, fit, function, reliability or quality

**Data Sheet Revision**

A new datasheet will be issued

**Disposition of Old Products**

Existing inventory will be shipped until depleted

The Datasheet changes won't affect the PCA8561 product inventory.

**Remarks**

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The PCA8561 device functionality won't be affected by these changes.

**Contact and Support**

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For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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NXP Semiconductors  
High Tech Campus, 5656 AG Eindhoven, The Netherlands

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**Affected OPN**

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