

PCN Number:	20190129001.0	PCN Date:	Feb. 6, 2019
Title:	Datasheet for MSP430FR2033, MSP430FR2032		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



MSP430FR2033, MSP430FR2032

SLASE45D – OCTOBER 2014 – REVISED JANUARY 2019

Changes from August 29, 2018 to January 17, 2019	Page
• Throughout the document, changed Modulation Oscillator (MODOSC) to Modulation Oscillator Clock (MODCLK)....	<u>1</u>
• Added "or memory corruption" to note (1) in Section 5.1, Absolute Maximum Ratings	<u>14</u>
• Added the note that begins "The VLO clock frequency is reduced by..." after Table 5-6, Internal Very-Low-Power Low-Frequency Oscillator (VLO)	<u>22</u>
• Changed the R _i parameter symbol to R _{i,MUX} in Table 5-17, ADC, Power Supply and Input Range Conditions	<u>30</u>
• Added the R _{i,Misc} parameter in Table 5-17, ADC, Power Supply and Input Range Conditions	<u>30</u>
• Removed ADCDIV from the formula for the t _{CONVERT} TYP value, because ADCCLK is after division, in Table 5-18, ADC, 10-Bit Timing Parameters	<u>30</u>
• Added note (2) for R _i calculation in Table 5-18, ADC, 10-Bit Timing Parameters	<u>30</u>
• Removed "±3°C" on both temperatures in the note that begins "The device descriptor structure contains..." in Table 5-19, ADC, 10-Bit Linearity Parameters	<u>31</u>
• Add "10b" for ADCSSEL bit in Table 6-6, Clock Distribution	<u>38</u>
• Added Figure 6-1, Clock Distribution Block Diagram	<u>38</u>
• Corrected the spelling of the IRDSSEL bit in the paragraph that begins "The IR functions are controlled by..." in Section 6.9.8, Timers (Timer0_A3, Timer1_A3)	<u>43</u>
• Changed two instances of "ADC 1.5-V Reference Temperature" to "ADC 1.5-V Reference Temperature Sensor" in Table 6-29, Device Descriptors	<u>61</u>

The datasheet number will be changing.

Device Family	Change From:	Change To:
MSP430FR2033, MSP430FR2032	SLASE45A	SLASE45D

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/MSP430FR2032>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

MSP430FR2032IG48	MSP430FR2032IG48R	MSP430FR2032IG56	MSP430FR2032IG56R
MSP430FR2032IPMR	MSP430FR2033IG48	MSP430FR2033IG48R	MSP430FR2033IG56
MSP430FR2033IG56R	MSP430FR2033IPM	MSP430FR2033IPMR	

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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